

<b>Notice of References Cited</b>	Application/Control No. 10/782,782	Applicant(s)/Patent Under Reexamination LEE ET AL.	
	Examiner Asok K. Sarkar	Art Unit 2891	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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	B	US-6,706,592	03-2004	Chern et al.	438/257
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
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	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf, "Silicon processing for the VLSI Era, Vol. 2, p 238 (1990), Lattice Press, CA
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.